

**MONDAY Afternoon April 12, 2010**

Lunch will be served at noon buffet style for you to enjoy in Barbados

Dr. Patrick Simpkin Director of Engineering KSC will speak at 12:20 pm in Barbados followed by a Q&A session

Sessions will start promptly at 1:05 pm

1.10-2.50	Session (3) Aruba	Tutorial Session (3) Barbados	Session (4) Antiqua
	<b>Power Amplifiers (1) (Chair: Simon Wood; Larry Dunleavy)</b>	<b>Chair: Ray Pengelly</b>	<b>Circuit Integration &amp; Tech. (Chair: Richard Abrahams)</b>
1.10-1.30	A MMIC Power Amplifier for Wireless Communications Amin Ezzedine and Ho Huang (AMCOM Communications Inc.)	Present and Future Filter Design Philosophy: Paradigm Shift in Progress <i>Dr. Richard Snyder, RS Microwave</i>	Ultra-Broadband High-Speed Integration Techniques for Circuits and Systems Design Rhonda Franklin Drayton (University of Minnesota)
1.30-1.50	A 1 KW PULSED AMPLIFIER USING MESFET, GAN, LDMOS and BIPOLAR Transistors at 2856 MHz Sarjit S Bharj (Princeton Microwave Technology Inc.)		BSIM4-Based Lateral Diode Model for RF ESD Applications Ming-Ta Yang (Qualcomm)
1.50-2.10	Parametric Power Amplifiers: Old Ideas, New Technologies J. Stevenson Kenney (Georgia Institute of Technology)		Transmitter Architecture for the Evaluation of Beamforming Schemes in the IEEE 802.11n Standard Michel Thériault (Laval University, Canada)
2.10-2.30	A Neural Network On-Line Adaptive Predistorter for Power Amplifier Chan Wang Park (University of Quebec in Rimouski, Canada)		Thermal Optimization of a Flip-Chip WiFi RF Front-End IC Kirk Laursen (Epic Communications, Inc., US)
2.30-2.50	High Power Load Pull with X-Parameters – A New Paradigm for Modeling and Design Gary Simpson (Maury)		Methodology of Design of Millimeter Wave Power Amplifiers complying with 125°C Electromigration Design Rules in advanced CMOS technology Thomas Quemerais (Grenoble Institute of Technology, France)
<b>Coffee Break</b>			
3.05-4.45	Session (5) Aruba	Tutorial Session (4) Barbados	Session (6) Antiqua
	<b>Antennas (Chair: Gokhan Mumcu; Michael Imparato)</b>	<b>Chair: Tom Brazil</b>	<b>Circuit Design Techniques (Chair: Jenshan Lin; Changzhi Li)</b>
3.05-3.25	A Series-Fed Coplanar Waveguide Slot Antenna Array James McKnight (University of South Florida)	Engineering and Measuring RF Waveforms -the Unifying Link Between System Performance, Circuit Design and Transistor Technology <i>Prof. Paul Tasker, Cardiff University, UK</i>	An Analytic Technique for Trade-Off of Noise Measure and Mismatch Loss for Low Noise Amplifier Design Alan Victor (North Carolina State University)
3.25-3.45	Evaluating the Robustness of Tunable Adaptive Antenna Arrays Benjamin Horwath (Santa Clara University, US)		A Wide-Band Differential and Single-Ended Microwave Amplitude Detector William Eisenstadt (UFL)
3.45-4.05	Smart Antenna using MTM-MEMS Georgina Rosas (INAOE, Mexico)		4-8 GHz LNA Design for an Adaptive Small Satellite Transponder Using InGaAs PHEMT Technology Ekpo (University of Manchester)
4.00-4.20	Performance of an Embedded Monopole Antenna Array in a UAV wing structure Mohammad S. Sharawi (King Fahd University of Petroleum and Minerals, Saudi Arabia)		Truly Differential RF and Microwave Front-End Design Shaofang Gong (Linköping University, Sweden)
4.25-4.45	Novel Chip Antenna with Eight Spiral Monopole Slot Structures for DVB-H Applications En-Tsao Chang (National Taipei University of Technology, TW)		24 GHz Low Phase Noise HBT DR Oscillator Photos Vryonides (Frederick University, Cyprus)

**Exhibitors St Martin/St Lucia**

5:30-6:45	<b>Student Poster Session (not reviewed)</b> <b>Chair: Paul G Flikkema, Joel Johnson</b>
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6:45-8:30	<b>Banquet Oceanside Deck (Weather Permitting) or Barbados</b> <b>Presentation of Rudolf E. Henning Mentoring Award</b>
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